Physics-Based Passivity-Preserving Parameterized Model Order Reduction for PEEC Circuit Analysis

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Abstract—The decrease of IC feature size and the increase of operating frequencies require 3D electromagnetic methods, such as the Partial Element Equivalent Circuit (PEEC) method, for the analysis and design of high-speed circuits. Very large systems of equations are often produced by 3D electromagnetic methods and model order reduction (MOR) methods were proven to be very effective in combating such high complexity. During the circuit synthesis of large-scale digital or analog applications, it is important to predict the response of the circuit under study as a function of design parameters, such as geometrical and substrate features. Traditional MOR techniques perform model order reduction only with respect to frequency, therefore the computation of a new electromagnetic model and corresponding reduced model is needed each time a design parameter is modified, reducing the CPU efficiency. Parameterized model order reduction (PMOR) methods become necessary to reduce large systems of equations with respect to frequency and other design parameters of the circuit, such as geometrical layout or substrate characteristics.

We propose a novel PMOR technique applicable to PEEC analysis which is based on a parameterization process of matrices generated by the PEEC method and the projection subspace generated by a passivity-preserving MOR method. The proposed PMOR technique guarantees overall stability and passivity of parameterized reduced order models over a user defined range of design parameter values. Pertinent numerical examples validate the proposed PMOR approach.

Index Terms—Partial Element Equivalent Circuit method (PEEC), parameterized model order reduction (PMOR), interpolation, passivity.

I. INTRODUCTION

Electromagnetic (EM) methods [1]–[3] have become increasingly indispensable analysis and design tools for a variety of complex high-speed systems. The use of these methods usually results in very large systems of equations which are prohibitively expensive to solve. Hence, model order reduction (MOR) techniques are crucial to reduce the complexity of EM models and the computational cost of the simulations, while retaining the important physical features of the original system [4]–[7]. The development of a reduced order model (ROM) of EM systems has become a topic of intense research over the last years, with applications to vias, high-speed packages, interconnects, and on-chip passive components [8]–[11]. An increasing popularity among electromagnetic compatibility engineers has been achieved by the Partial Element Equivalent Circuit (PEEC) method, since it is able to transform the EM system under examination into a passive RLC equivalent circuit. PEEC uses a circuit interpretation of the Electric Field Integral Equation (EFIE) [12], thus allowing to handle complex problems involving EM fields and circuits [2], [13], [14]. Nonlinear circuit devices such as drivers and receivers are usually connected with PEEC equivalent circuits using a time domain circuit simulator (e.g. SPICE [15]). However, inclusion of the PEEC model directly into a circuit simulator may be computationally intractable for complex structures, because the number of circuit elements can be in the tens of thousands. In this case, a first solution consists in the use of fast multipole methods [16], [17]. The drawback of these techniques relies on the fact that they are dependent on the Green’s function of the problem. Another option is represented by MOR techniques which are adopted to reduce the size of the PEEC model [7], [18], [19].

Traditional MOR techniques perform model reduction only with respect to frequency. However, during the circuit synthesis of large-scale digital or analog applications, it is also important to predict the response of the circuit under study as a function of design parameters, such as geometrical and substrate features. A typical design process includes optimization and design space exploration, and thus requires repeated simulations for different design parameter values. Such design activities call for parameterized model order reduction (PMOR) methods that can reduce large systems of equations with respect to frequency and other design parameters of the circuit, such as geometrical layout or substrate characteristics.

Over the years, a number of PMOR methods have been developed. In order to model and analyze interconnect behavior with process variations, various techniques have been proposed for variational interconnect order reduction [20], [21]. These approaches apply projection operator and generate reduced-order interconnect models. In addition, the projection subspace and/or the reduced-order system matrices are approximated as low-order polynomials of process parameters such that the process variation effects can be incorporated into the interconnect model. These process parameters, for example, can be the width and thickness of the interconnect metal wires.
The authors in [22] propose to approximate the system transfer function by low-order polynomials of process parameters, instead of the projection subspace and/or the reduced order system matrices. The algorithm described in [22] computes the projection subspace and generates parameterized ROMs such that the multiparameter moments are matched. However, the structure of such method may present some computational problems, and the resulting parameterized ROMs usually suffer from oversize when the number of moments to match is high, either because high accuracy (order) is required or because the number of parameters is large. The Compact Order Reduction for parameterized Extraction (CORE) algorithm [23] applies a two-step explicit-and-implicit scheme for multiparameter moment matching. It is numerically stable, but unfortunately it does not preserve passivity. The Parameterized Interconnect Macromodeling via a two-directional Arnoldi process (PIMTAP) algorithm presented in [24] is numerically stable, preserves the passivity of parameterized RLC networks, but, such as all multiparameter moment-matching based PMOR techniques, it is suitable only to a low-dimensional design space.

This paper proposes a PMOR method applicable to PEEC analysis which is based on a parameterization process of matrices generated by the PEEC method and the projection subspace generated by a passivity-preserving MOR method. The Laguerre-SVD MOR method [19] is used in this paper. Overall stability and passivity of parameterized ROMs are guaranteed by construction over the design space of interest. PEEC models and parameterized ROMs describe an admittance (Y) representation. However, it should be noted that the proposed PMOR technique is not bound to the Laguerre-SVD method, other passivity-preserving MOR techniques based on a projection subspace approach can be used, such as the PRIMA method [7].

The paper is organized as follows. Section II describes the modified nodal analysis (MNA) equations of the PEEC method. Section III describes the proposed PMOR method. Finally, some pertinent numerical examples validate the proposed technique in Section IV.

II. PEEC FORMULATION


The main difference of the PEEC method with other integral equation based techniques such as the Method of Moments (MoM) [1] resides in the fact that it provides a circuit interpretation of the EFIE [12] in terms of partial elements, namely resistances, partial inductances and coefficients of potential. Thus, the resulting equivalent circuit can be studied by means of SPICE-like circuit solvers [15] in both time and frequency domain.

Over the years, several improvements of the PEEC method have been performed thus allowing to handle complex problems involving both circuits and electromagnetic fields [2], [13], [14], [25]–[28].

In the standard approach, volumes and surfaces are discretized into elementary regions, hexahedra and patches respectively [27] over which the current and charge densities are expanded into a series of basis functions. Pulse basis functions are usually adopted as expansion and weight functions. Such choice of pulse basis functions corresponds to assume constant current density and charge density over the elementary volume (inductive) and surface (capacitive) cells, respectively.

Following the standard Galerkin’s testing procedure, topological elements, namely nodes and branches are generated and electrical lumped elements are identified modeling both the magnetic and electric field coupling.

Conductors are modeled by their ohmic resistance, while dielectrics requires modeling the excess charge due to the dielectric polarization [29]. Magnetic and electric field coupling are modeled by partial inductances and coefficients of potential, respectively.

The magnetic field coupling between two inductive volume cells $\alpha$ and $\beta$ is described by the partial inductance

$$L_{p_{\alpha\beta}} = \frac{\mu}{4\pi} \int_{S_{\alpha}} \int_{S_{\beta}} \frac{1}{R_{\alpha\beta}} dA_{\alpha} dA_{\beta}$$

(1)

where $R_{\alpha\beta}$ is the distance between any two points in volumes $u_{\alpha}$ and $u_{\beta}$ with $a_{\alpha}$ and $a_{\beta}$ their cross sections. The electric field coupling between two capacitive surface cells $\delta$ and $\gamma$ is modeled by the coefficient of potential

$$P_{\delta\gamma} = \frac{1}{4\pi\varepsilon} \int_{S_{\delta}} \int_{S_{\gamma}} \frac{1}{R_{\delta\gamma}} dS_{\delta} dS_{\gamma}$$

(2)

where $R_{\delta\gamma}$ is the distance between any two points on surfaces $\delta$ and $\gamma$, while $S_{\delta}$ and $S_{\gamma}$ denote the area of their respective surfaces.

Generalized Kirchoff’s laws, for conductors, can be rewritten as

$$P^{-1} \frac{dv(t)}{dt} - A^T i(t) + i_e(t) = 0$$

(3a)

$$-A v(t) - L_p \frac{di(t)}{dt} - Ri(t) = 0$$

(3b)

where $A$ is the connectivity matrix, $v(t)$ denotes the node potentials to infinity, $i(t)$ and $i_e(t)$ represent the currents flowing in volume cells and the external currents, respectively.

When dielectrics are considered, the resistance voltage drop $R_{\delta\gamma}$ is substituted by the excess capacitance voltage drop that is related to the excess charge by $v_{d(t)} = C_{d}^{-1} q_{d(t)}$ [29].

Hence, for dielectric elementary cells, equations (3) become

$$P^{-1} \frac{dv(t)}{dt} - A^T i(t) + i_e(t) = 0$$

(4a)

$$-A v(t) - L_p \frac{di(t)}{dt} - v_{d(t)} = 0$$

(4b)

$$i(t) = C_d \frac{dv_{d(t)}}{dt}$$

(4c)

A selection matrix $K$ is introduced to define the port voltages by selecting node potentials. The same matrix is used to obtain the external currents $i_{p}(t)$ by the currents $i_{e}(t)$ which are of opposite sign with respect to the $n_p$ port currents $i_{p}(t)$

$$v_p(t) = K v(t)$$

(5a)

$$i_e(t) = K^T i_p(t).$$

(5b)

An example of PEEC circuit electrical quantities for a conductor elementary cell is illustrated, in the Laplace domain,
in Fig. 1 where the current controlled voltage sources $sL_{p,ij}I_j$ and the current controlled current sources $I_{cc}$ model the magnetic and electric field coupling, respectively. Hence, if the MNA approach [31] is used, branches where currents flow. Among the latter, we denote magnetic and electric field coupling, respectively.

Let us assume that the system under analysis consists of conductors and dielectrics. Let the current and charge density be defined in volumes and surface of conductors and dielectrics. Let the current and charge under voltage excitation.

In a more compact form, the previous equations (9) can be rewritten as

$$
\begin{align*}
\text{C} \frac{dx(t)}{dt} &= -Gx(t) + Bu(t) \\
i_p(t) &= I^T x(t)
\end{align*}
$$

where $x(t) = [q(t) \ i(t) \ v(t) \ i_s(t)]^T$. Since this is an $n_p$-port formulation, whereby the only sources are the voltage sources at the $n_p$-port nodes, $B = I$ where $B \in \mathbb{R}^{n_x \times n_p}$.

B. Scaling

The system of equations (9) is typically ill-conditioned because charges are usually much smaller than currents and voltages. Correspondingly, the entries of the matrix $P$ are larger than other elements in matrices $C$ and $G$ by several orders of magnitude. The ill-conditioning of (9) prevents MOR methods to be efficiently applied. In order to mitigate such a problem, scaling can be adopted. The units of the electrical quantities are changed consistently as shown in Table I.

C. Properties PEEC formulation

In order to apply the proposed PMOR technique, it is important to specify the properties of the matrices involved in the PEEC formulation (9).

Both matrices describing electric and magnetic field coupling, $P$ and $L_p$ respectively, are full symmetric matrices. In
the case of orthogonal geometries, mutual partial inductances corresponding to orthogonal currents are equal to zero. Even in this case, rows and columns can be recast so that the partial inductance matrix \( \mathbf{L}_p \) is block-diagonal. Since each block is symmetric positive definite, the overall matrix \( \mathbf{L}_p \) is symmetric positive definite as well. The coefficient of potential matrix \( \mathbf{P} \) is also symmetric positive definite [32].

When pulse basis functions are used, as is in the standard PEEC formulation [2], resistance and excess capacitance matrices, \( \mathbf{R} \) and \( \mathbf{C}_t \) respectively, are diagonal and symmetric positive semidefinite and definite. The matrix \( \mathbf{R} \) is diagonal, with positive diagonal elements corresponding to conductor elementary cells, while the diagonal elements corresponding to dielectric elementary cells are equal to zero. The matrix \( \mathbf{C}_t \) is diagonal with all the diagonal elements positive.

Assuming the previous matrix properties, it is easy to prove that the matrices \( \mathbf{C} \), \( \mathbf{G} \) satisfy the following properties

\[
\mathbf{C} = \mathbf{C}^T \geq 0 \quad (11a)
\]

\[
\mathbf{G} + \mathbf{G}^T \geq 0 \quad (11b)
\]

The properties of the PEEC matrices \( \mathbf{B} = \mathbf{L} \), \( \mathbf{C} = \mathbf{C}^T \geq 0 \), \( \mathbf{G} + \mathbf{G}^T \geq 0 \) ensure the passivity of the PEEC admittance model \( \mathbf{Y}(s) = \mathbf{L}^T(s\mathbf{C} + \mathbf{G})^{-1} \mathbf{B} \) [33] and allow to exploit the passivity-preserving capability of the Laguerre-SVD MOR algorithm [19]. When performing transient analysis, stability and passivity must be guaranteed. It is known that, while a passive system is also stable, the reverse is not necessarily true [34], which is crucial when the reduced model is to be utilized in a general-purpose analysis-oriented nonlinear simulator (e.g., SPICE). Passivity refers to the property of systems that cannot generate more energy than they absorb through their electrical ports. When the system is terminated on any arbitrary passive loads, none of them will cause the system to become unstable [35], [36].

### III. PARAMETERIZED MODEL ORDER REDUCTION

In this section we describe a PMOR algorithm that is able to include, in addition to frequency, \( N \) design parameters \( \mathbf{g} = (g^{(1)}, ..., g^{(N)}) \) in a parameterized ROM, such as the layout features of a circuit (e.g., lengths, widths,...) or the substrate parameters (e.g., thickness, dielectric permittivity, losses,...). The main objective of the PMOR method is to accurately approximate the original scalable system (having a high complexity) with a reduced scalable system (having a low complexity) by capturing the behavior of the original system with respect to frequency and other design parameters. The design space \( \mathcal{D}(\mathbf{g}) \) is considered as the parameter space \( \mathcal{P}(s, \mathbf{g}) \) without frequency. The parameter space \( \mathcal{P}(s, \mathbf{g}) \) contains all parameters \( (s, \mathbf{g}) \). If the parameter space is \( (N+1) \)-dimensional, the design space is \( N \)-dimensional. The proposed algorithm guarantees stability and passivity of a parameterized ROM over the entire design space of interest. Two data grids are used in the modeling process: an estimation grid and a validation grid. The first grid is utilized to build parameterized ROMs, while the second grid, more dense than the previous one, is utilized to assess the capability of parameterized ROMs of describing the system under study in points of the design space previously not used for its construction. To clarify the use of these two design space grids, we show in Fig. 2 a possible estimation and validation design space grid in the case of two design parameters \( \mathbf{g} = (g^{(1)}, g^{(2)}) \).

#### A. PMOR algorithm

Considering the influence of the design parameters \( \mathbf{g} = (g^{(1)}, ..., g^{(N)}) \), the MNA formulation (10a)-(10b) becomes

\[
\mathbf{C}(\mathbf{g}) \frac{d\mathbf{x}(t, \mathbf{g})}{dt} = -\mathbf{G}(\mathbf{g})\mathbf{x}(t, \mathbf{g}) + \mathbf{B}\mathbf{u}(t) \quad (12a)
\]

\[
\mathbf{i}_p(t, \mathbf{g}) = \mathbf{L}^T\mathbf{x}(t, \mathbf{g}) \quad (12b)
\]

We assume that a topologically fixed discretisation mesh is used and it is independent from the specific design parameters values. It preserves the size of the system matrices as well as the numbering of the mesh nodes and mesh edges. The mesh is only locally stretched or shrunk, when shape parameters are modified. In general, the global coordinates of the nodes as well as the length and orientation of the edges of the topologically fixed mesh change when shape parameters change; however, these changes are neither introducing new state variables nor eliminating existing state variables. The matrices \( \mathbf{B}, \mathbf{L}^T \) are uniquely determined by the circuit topology and therefore remain constant, while the matrices \( \mathbf{C} \) and \( \mathbf{G} \) are defined as functions of the design parameters. At a deeper level in the MNA equations (12a)-(12b), the previous assumptions lead to have \( \mathbf{P}(\mathbf{g}), \mathbf{L}_p(\mathbf{g}), \mathbf{C}_d(\mathbf{g}), \mathbf{R}(\mathbf{g}) \), while the other internal PEEC matrices \( \mathbf{A}, \Phi, \mathbf{K} \) are constant. The proposed PMOR method starts from computing the multivariate models \( \mathbf{P}(\mathbf{g}), \mathbf{L}_p(\mathbf{g}), \mathbf{C}_d(\mathbf{g}), \mathbf{R}(\mathbf{g}) \) guaranteeing some matrix properties, as explained in Section III-B.

When the multivariate models \( \hat{\mathbf{P}}(\mathbf{g}), \hat{\mathbf{L}}_p(\mathbf{g}), \hat{\mathbf{C}}_d(\mathbf{g}), \hat{\mathbf{R}}(\mathbf{g}) \) are computed, instead of assembling a PEEC model and performing a MOR step for each point of interest \( \tilde{\mathbf{g}} = (g_{k_1}^{(1)}, ..., g_{k_N}^{(N)}) \) in the design space, the Laguerre-SVD MOR method [19] is applied to each PEEC model related to the estimation design space grid.
• choose a value for $\alpha$ (positive scaling parameter of Laguerre basis functions) and the reduced order $q$;
• solve $(G + \alpha C)Q_0 = B$;
• for $k = 1, \ldots, q - 1$ solve $(G + \alpha C)Q_k = (G - \alpha C)Q_{k-1}$
• $K_r = [Q_0, \ldots, Q_{q-1}]$;

and a corresponding set of Krylov matrices $K_r(g)$. The sampling density in the estimation design space grid is important to accurately describe the parameterized behavior of an EM system under study over the entire design space of interest. A technique to choose the number of points in the estimation grid can be found in [37]. Once the multivariate models $P(g), L_p(g), C_d(g), R(g), K_r(g)$ are built, a PEEC model $Y(s, \bar{g}) = \mathbf{L}^T (sC(\bar{g}) + \mathbf{G}(\bar{g}))^{-1} \mathbf{B}$ can be computed and a projection matrix $U(\bar{g})$ can be computed by means of the singular value decomposition [38] of $K_r(\bar{g})$ [19] for any point $\bar{g} = (g^{(1)}_{k_1}, \ldots, g^{(N)}_{k_N})$

$$U(\bar{g}) \Sigma(\bar{g}) \bar{V}(\bar{g})^T = \text{SVD}[K_r(\bar{g})]. \quad (13)$$

Finally, a congruence transformation is applied on $C(\bar{g}), G(\bar{g}), L, B$ using $U(\bar{g})$ [19]

- $C_r(\bar{g}) = U(\bar{g})^T C(\bar{g}) U(\bar{g})$ \quad (14a)
- $G_r(\bar{g}) = U(\bar{g})^T G(\bar{g}) U(\bar{g})$ \quad (14b)
- $B_r(\bar{g}) = U(\bar{g})^T B$ \quad (14c)
- $L_r(\bar{g}) = U(\bar{g})^T L$ \quad (14d)

to obtain the parameterized reduced model. A flowchart that describes the different steps of the proposed PMOR method is shown in Fig. 3. Concerning the reduced order, which represents the column dimension of $K_r(\bar{g})$ and $U(\bar{g})$, it is chosen by a bottom-up approach: it is increased as long as a certain RMS-error threshold is satisfied in the validation design space grid.

**B. Multivariate interpolation of the internal PEEC matrices**

Starting from multivariate data samples $\{g_k, P(g_k), L_p(g_k), C_d(g_k), R(g_k), K_r(g_k)\}_{k=1}^{K_{\text{data}}}$ the multivariate models $P(g), L_p(g), C_d(g), R(g), K_r(g)$ are built. While the interpolation process of the set of Krylov matrices is performed without any constraint, the multivariate models of the internal PEEC matrices preserve the positive definiteness of $P(g), L_p(g), C_d(g)$ and the positive semidefiniteness of $R(g)$. Consequently, the properties (11a)-(11b) of an admittance PEEC model and its characteristics and it performs elementwise interpolation. Unfortunately, although ordinary spline schemes are generally well behaved, they do not prevent overshoot and undesired oscillations at intermediate points, that can violate inherited data features as positivity. Some modified spline interpolation

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**Fig. 3. Flowchart of the proposed PMOR method.**
schemes that are able to preserve positivity of the data samples in the univariate case are described in [43]–[45]. Another simpler and straightforward approach to preserve positivity using the ordinary splines is proposed in this paper and it is composed of three steps: 1) an analytical mapping of the data samples is performed, 2) the new data samples are interpolated using ordinary splines without any constraint, 3) the interpolated data samples are transformed back by the inverse mapping. The mapping function has to be able to ensure the positivity of the interpolated data samples after the inverse mapping. For any positive diagonal matrix entry \( f(g) \) of the matrices \( C, R(g) \) under modeling, the following mapping function is used

\[
M(g) = \log \left( \frac{f(g)}{\min(f(g))} \right), \quad g \in \{g_k\}_{k=1}^{K_{tot}}
\]

Once the transformed data samples are modeled by using multivariate splines, the inverse mapping function

\[
M_{inv}(g) = \min(f(g)) \exp(M(g)), \quad g \in \{g_k\}_{k=1}^{K_{tot,interp}}
\]

is used for the back transformation. It is straightforward to verify that the following procedure ensure the positivity of the final interpolated values. While a diagonal matrix is positive definite if and only if all the diagonal elements are positive, a non-diagonal matrix requires more general conditions. The matrix \( P(g) \) is full, symmetric and positive definite, while the matrix \( L_p(g) \) is symmetric, positive definite, and in general a certain degree of sparsity can be present, due to orthogonal elementary cells. It is easy to show that multivariate interpolation schemes that belong to the class of positive interpolation operators [39]–[41] are able to preserve the positive definiteness property, when they are applied to positive definite matrices. When the interpolation of positive definite non-diagonal matrices is performed elementwise by schemes with kernel functions that depend on the matrices used in the interpolation process (e.g. multivariate cubic spline interpolation), the following procedure can be used to guarantee the positive definiteness property. Let us denote

\[
S(R) = \{ Q \in \mathcal{M}(R), Q^T = Q \}
\]

the space of all \( R \times R \) real symmetric matrices with \( \mathcal{M}(R) \) the space of \( R \times R \) real matrices and

\[
\mathcal{P}(R) = \{ Q \in S(R), Q > 0 \}
\]

the space of all \( R \times R \) real symmetric positive-definite matrices. It is well known that the matrix exponential is a one-to-one map from \( S(R) \) to \( \mathcal{P}(R) \). In other words, the matrix exponential of any real symmetric matrix is a real symmetric positive-definite matrix, and the inverse of the matrix exponential (i.e., principal matrix logarithm) of any real symmetric positive-definite matrix is a real symmetric matrix [46], [47]. Exploiting such property of the exponential map, the matrices \( P(g), L_p(g) \) that are symmetric and positive definite are mapped from \( \mathcal{P}(R) \) to \( S(R) \) using the principal matrix logarithm operator, then only the lower or upper triangular part is interpolated elementwise using the ordinary splines. Finally, the matrices are mapped back by the matrix exponential operator which results in symmetric positive definite matrices, therefore the original properties of the matrices \( P(g), L_p(g) \) are preserved. We propose a multivariate interpolation process that is able to preserve the positive definiteness of \( P(g), L_p(g), C_d(g) \) and the positive semidefiniteness of \( R(g) \). Consequently, the properties (11a)-(11b) of the admittance PEEC model \( Y(s,g) = L^T(sC(g) + G(g))^{-1}B \) and its related passivity are satisfied for any point \( g = (g_1, ..., g_N) \) over the design space. The overall computational complexity of the presented PMOR algorithm can be divided into: 1) complexity of computing the multivariate models of \( P(g), L_p(g), C_d(g), R(g) \) by interpolation, 2) complexity of the SVD operation on \( L_p(g) \) to obtain the projection matrix \( U(g) \), 3) complexity of the congruence transformation by means of \( U(g) \). Which step is the most computationally expensive cannot be established in advance, since the computational complexity of the interpolation process depends on the chosen interpolation scheme. Concerning the SVD operation, it can be replaced by cheaper modified Gram-Schmidt (MGS) and Householder QR (HQR) operations [19], [38], which are computationally cheaper.

C. Passivity assessment considerations

The properties of the PEEC matrices, the multivariate interpolation approach and the Laguerre-SVD MOR algorithm ensure overall stability and passivity for a parameterized ROM \( Y_r(s,g) \) by construction. Although no passivity check is required for \( Y_r(s,g) \), the authors describe in this section a passivity test for the sake of completeness. Let us assume that \( Y_r(s,g) \) is obtained and one wants to perform a passivity test for a specific point \( \hat{g} \) in the design space. If the descriptor matrix \( C_r \) of \( Y_r(s,\hat{g}) \) is singular, the procedure described in [48] is used to convert the descriptor system into a standard state-space model

\[
\begin{align*}
\frac{dx(t)}{dt} &= Ax(t) + Bu(t) \quad (21a) \\
y(t) &= Cx(t) + Du(t) \quad (21b)
\end{align*}
\]

otherwise the standard state-space model can be obtained by

\[
\begin{align*}
A &= -C_r^{-1}G_r \\
B &= C_r^{-1}B_r \\
C &= B_r^T \\
D &= D_r
\end{align*}
\]

Once \( Y_r(s,\hat{g}) \) is transformed into a standard state-space form, its passivity can be verified by computing the eigenvalues of an associated Hamiltonian matrix [49]

\[
\hat{H} = \begin{bmatrix}
A - BR^{-1}C & BR^{-1}B^T \\
-C^T R^{-1}C & -A^T + C^T R^{-1}B^T
\end{bmatrix}
\]

(23)

with \( R = D + D^T \). The system \( Y_r(s,\hat{g}) \) is passive if \( \hat{H} \) has no purely imaginary eigenvalues. This passivity test can only be applied if \( D + D^T \) is not singular. If such singularity exists,
the modified Hamiltonian-based passivity check proposed in [50] should be used.

IV. NUMERICAL RESULTS

This section presents two numerical examples that validate the proposed PMOR method. Let us define the weighted RMS-error as:

\[ Err(g) = \sqrt{\sum_{i=1}^{N_{\text{port}}} \sum_{k=1}^{K_s} w_{Y_i}(s_k, g) (Y_{r,i}(s_k, g) - Y_i(s_k, g))^2} \]

(\(N_{\text{port}}\))^2\(K_s\) \hspace{1cm} (24)

with

\[ w_{Y_i}(s, g) = |(Y_i(s, g))^{-1}| \]

(25)

where \(N_{\text{port}}\) is the number of system ports and \(K_s\) is the number of frequency samples. The worst case RMS-error over the validation grid is chosen to assess the accuracy and the quality of parameterized ROMs.

\[ g_{\text{max}} = \arg \max_g Err(g), \ g \in \text{validation grid} \]

(26)

\[ Err_{\text{max}} = Err(g_{\text{max}}) \]

(27)

and it is used in the numerical examples. The proposed PMOR algorithm was implemented in Matlab R2009A [51] and all experiments were carried out on Windows platform on Intel Core2 Extreme CPU Q9300 2.53GHz machines with 8GB RAM.

A. Two coupled microstrips with variable spacing

Two coupled microstrips (length \(L = 2\) cm) have been modeled in this example. The cross section is shown in Fig. 4. The conductors have width \(W = 500\) \(\mu\)m and thickness \(t = 50\) \(\mu\)m, the dielectric is \(800\) \(\mu\)m thick. A bivariate ROM is built as a function of the spacing \(S\) between the microstrips in addition to frequency. Their corresponding ranges are shown in Table II.

![Cross section of the coupled microstrips.](image)

The PEEC method is used to compute the \(C, G, B, L\) matrices in (10a)-(10b) for 25 values of the spacing. The order of all original PEEC models is equal to \(n_u = 2640\). The multivariate models \(\Phi_g(g), L_p(g), C_d(g), R(g), K_r(g)\) are computed by spline interpolation using only 9 spacing values and with a CPU time equal to 9.6 s. Then, the bivariate ROM \(Y_{r}(s, S)\) is obtained with a reduced order \(q = 38\). Fig. 5 shows the magnitude of the parameterized ROM of \(Y_{11}(s, S)\), while Fig. 6 compares the magnitude of \(Y_{11}(s, S)\) and \(Y_{12}(s, S)\) and their parameterized ROMs for the spacing values \(S = \{1.125, 2.375, 3.875\}\) mm. These specific spacing values have not been used during the construction of the multivariate models \(\Phi_g(g), L_p(g), C_d(g), R(g), K_r(g)\), nevertheless an excellent agreement between model and data can be observed. The worst case RMS-error defined in (27) is equal to \(1.8 \cdot 10^{-2}\) and it occurs for \(g_{\text{max}} = S = 3.875\) mm. Fig. 7 shows the minimum absolute value of the real part of the Hamiltonian matrix eigenvalues over a dense sweep of the design space. Since there are no purely imaginary eigenvalues, the parameterized ROM is passive over the design space of interest. As clearly seen, the parameterized ROM captures the behavior of the system very accurately, while guaranteeing stability and passivity over the entire design space.

![Magnitude of the bivariate ROM of \(Y_{11}(s, S)\).](image)

B. Spiral inductor with variable horizontal and vertical length

An integrated spiral inductor has been modeled in this example. The structure is shown in Fig. 8. The conductors width is equal to \(46\) \(\mu\)m. A trivariate ROM is built as a function of the horizontal \(L_x\) and vertical \(L_y\) length of the spiral inductor in addition to frequency. Their corresponding ranges are shown in Table III.

The PEEC method is used to compute the \(C, G, B, L\) matrices in (10a)-(10b) for 11 values of \(L_x\) and 11 values of \(L_y\). The order of all original PEEC models is equal to \(n_u = 801\). The multivariate models \(\Phi_g(g), L_p(g), C_d(g), R(g), K_r(g)\) are computed by spline interpolation using only 6 values
of $L_x$ and 6 values of $L_y$ and with a CPU time equal to 43.7 s. Then, the trivariate ROM $\mathbf{Y}_r(s, L_x, L_y)$ is obtained with a reduced order $q = 91$. Figs. 9-10 show the magnitude of the parameterized ROM of $\mathbf{Y}_{11}(s, L_x, L_y)$ for the vertical length values $L_y = \{0.46, 0.93\}$ mm, while

Fig. 11 compares the magnitude of $\mathbf{Y}_{11}(s, L_x, L_y)$ and its parameterized ROM for the horizontal and vertical length values $L_x = 0.63$ mm, $L_y = \{0.50, 0.63, 0.76\}$ mm. These specific horizontal and vertical length values have not been used during the construction of the multivariate models $\mathbf{P}(g), \mathbf{L}_p(g), \mathbf{C}_d(g), \mathbf{R}(g), \mathbf{K}_r(g)$, nevertheless an excellent agreement between model and data can be observed. The worst case RMS-error defined in (27) is equal to $5 \cdot 10^{-2}$ and it occurs for $g_{\text{max}} = \{L_x, L_y\} = \{0.86, 0.76\}$ mm. Fig. 12 shows the minimum absolute value of the real part of the Hamiltonian matrix eigenvalues over a dense sweep of the design space. Since there are no purely imaginary eigenvalues, the parameterized ROM is passive over the design space of interest. As in the previous example, the parameterized ROM is able to accurately describe the parameterized behavior of the system, while preserving overall stability and passivity.

V. CONCLUSIONS

We have presented a new PMOR technique applicable to PEEC analysis which is based on a parameterization process of matrices generated by the PEEC method and the projection subspace generated by the Laguerre-SVD MOR method. Overall stability and passivity of parameterized ROMs are guaranteed by construction over the design space of interest. Numerical examples have validated the proposed PMOR approach on practical application cases, showing that it is able to build very accurate parameterized ROMs of highly dynamic EM systems, while guaranteeing stability and passivity over the entire design space of interest.
Fig. 9. Magnitude of the trivariate ROM of $Y_{11}(s, L_x, L_y)$ ($L_y = 0.46$ mm).

Fig. 10. Magnitude of the trivariate ROM of $Y_{11}(s, L_z, L_y)$ ($L_y = 0.93$ mm).

Fig. 11. Magnitude of the trivariate ROM of $Y_{11}(s, L_x, L_y)$ ($L_x = 0.63$ mm, $L_y = 0.50$, $0.63$, $0.76$) mm.

Fig. 12. Minimum absolute value of the real part of the Hamiltonian matrix eigenvalues.

REFERENCES


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