Optimization of Ion Implantation Condition for Depletion-type Silicon optical modulators

Hui Yu, Wim Bogaerts, Member, IEEE, and An De Keersgieter

Abstract—We study the influence of the ion implantation conditions on the performance of depletion-type silicon optical modulators by a combined simulation of the process flow, the electrical characteristic and the beam propagation. Through calculations about different implantation positions, energies and tilt angles, our work reveals that a gap between specific implantation windows is able to alleviate the modulation efficiency degradation due to the lateral struggling of implanted ions, while a tilt angle reduces the optical loss without harming the modulation efficiency. After an optimization about the implantation condition, the extinction ratio of the Mach-Zehnder modulator can be improved by 4.6 dB, while its optical loss falls from 3 dB to 2.47 dB. Finally, a simplified doping pattern which eliminates 2 implantation steps is discussed.

Index Terms—Device modeling, ion implantation, optical modulation, plasma-dispersion effect, silicon photonics.

I. INTRODUCTION

Optical modulators which encode a bit stream onto the optical carrier are an essential functional component for any optical communication links ranging from telecommunications to optical interconnects. Among different modulation techniques, all-silicon modulators are promising due to their compatibility with mature CMOS technology. Since unstrained silicon exhibits no linear electro-optic effect [1] and only very weak Kerr and Franz-Keldysh effects, most high-speed silicon modulators rely on the carrier plasma dispersion effect [2], where a change in carrier concentration causes a change in absorption, and through the Kramer-Kronig relations, a change in refractive index. A carrier density variation can be reached by carrier injection in a forward biased p-i-n diode [3]-[5], accumulation in a MOS capacitor [6], [7] or depletion in a reverse biased p-n diode [8]-[13]. Among the three mechanisms, carrier depletion has an advantage on operation speed since it relies on the electric-field induced majority carrier dynamics. However, it suffers from low modulation efficiency due to a relatively small overlap between the carrier depletion region and the optical mode. A lot of efforts are directed to improve its modulation efficiency, such as increasing the doping concentration [9], utilizations of ring resonators [10], [11], or enhancing the overlap between the optical mode and the depletion region by interleaved p-n junctions perpendicular to the light propagation direction [8].

Besides improving the depletion-type modulator by better optical or electrical designs, we should also pay attention to the influence of practical processing conditions. A key step to manufacturing any depletion-type modulators is to implement p-n diodes inside the optical waveguide. In most cases this is achieved by ion implantation, which directly determines the doping profile and the consequent modulation characteristic. On the other hand, a variety of parameters that compose a practical implantation offer us a large space for a process optimization. In this paper, we simulate and optimize the implantation condition for depletion-type optical modulators with the aid of a commercially available process simulator which is able to model whole semiconductor manufacturing processes. This paper is organized as follows: In section II, we present our design of a typical abrupt p-n junction based optical phase shifter, which serves as a target structure of the following process optimization. In section III, we study the influence on the optical modulation of different implantation parameters, such as implantation position, energy and tilt angle. In section IV, we reduce 2 ion implantation steps without affecting the performance of the device. Finally a conclusion is reached in section V.

II. DEVICE DESIGN

Fig. 1(a) shows a schematic cross section of an ideal abrupt p-n junction based optical phase shifter. It is a shallow-etched rib waveguide with an embedded lateral p-n diode based on the silicon-on-insulator (SOI) platform. A phase difference would accumulate with respect to a reference waveguide as a beam passes through the phase shifter. A Mach-Zehnder interferometer then converts the phase difference into the final optical intensity modulation. The rib width and height are 500 nm and 220 nm respectively, while the etching depth is 70 nm for the compatibility with grating-based fiber coupling structures [14]. Two moderately doped slits with equal concentration N and opposite polarities form a p-n junction inside the rib waveguide. A driven voltage sweeps out free carriers inside the two slits, which compose an active region of the modulator. Since a density variation of holes induces a larger refractive index change than that of electrons, there is a

Manuscript received February 3, 2010.
H. Yu and W. Bogaerts are with the Photonics Research Group, Ghent University - imec, Department of Information Technology (INTEC), Sint-Pietersnieuwstraat 41, 9000 Gent, Belgium (e-mail: huy@intec.ugent.be).
W. Bogaerts acknowledges the Flemish Research Foundation for a postdoctoral fellowship.
A. D. Keersgieter is with imec, 3001, Leuven, Belgium.
small offset between the junction plane and the optical mode center. The optimal value is 0.04 μm for our structure in Fig. 1(a).

According to the optical waveguide theory, the effective mode index change $\Delta n_{eff}$ of a rib waveguide is proportional to the overlap integral between the optical mode and the local refractive index change $\Delta n(x,y)$ over the whole carrier depletion region:

$$\Delta n_{eff} = \frac{\iint \Delta n(x,y)|E(x,y)|^2 dxdy}{\iint |E(x,y)|^2 dxdy}$$

(1)

Where $E(x,y)$ is the electric field distribution of the optical mode. For the lateral p-n junction in Fig. 1(a), we only consider the integral in $x$ direction. Normally the scale of the optical mode is much larger than that of the depletion region (electrical field 1/e width of 0.85 μm for the rib waveguide here), thus the optical intensity can be regarded as a constant throughout the depletion region for a first approximation. Therefore, the effective mode index change $\Delta n_{eff}$ is approximately proportional to the product of the doping concentration $N$ and the depletion region width $w$: $\Delta n_{eff} \propto wN$.

On the other hand, the depletion region width is reverse proportional to the square root of the doping concentration: $w \propto 1/\sqrt{N}$ [15], so finally we can deduce that under a certain reverse bias the effective mode index change is proportional to the square root of the doping concentration: $\Delta n_{eff} \propto \sqrt{N}$. This relationship implies that we can enhance the modulation efficiency by increasing the doping concentration in the active region. Ideally, the active region can be made equal to the depletion region. Any excess of the active region over the depletion region does not contribute to the effective mode index change but increases the optical loss. However, the depletion region shrinks with increasing the doping concentration, while the smallest slit width is always limited by the practical lithography technology, so we cannot increasing the doping concentration in the active region without any limit. For our optical lithography system ASML PAS5500/1100 which uses an ArF laser at 193 nm for imaging, the smallest reliable wire width is about 140 nm [16]. We choose this value as the slit width and set a doping concentration of $10^{18}$/cm$^3$. A simple calculation shows that the depletion region inside both $n$ and $p$ type materials is 84.2 nm at a reverse bias of -10 V, which is much less than the slit width of 140 nm. We will discuss how to remedy this issue by a proper implantation condition in section III. In order to reduce the optical absorption loss, two heavily doped regions ($10^{20}$/cm$^3$) to form Ohmic contacts are situated 1 μm apart from the rib, and the two regions connecting the contact regions and the slits are lightly doped to $2 \times 10^{17}$/cm$^3$.

We will give an explanation about this value in section III.

III. IMPLANTATION CONDITION OPTIMIZATION

In this section we simulate how an implantation condition would impact the modulation characteristic. Before any specific simulation, it is necessary to introduce the whole simulation flow: At first, a two-dimensional process simulator DIOS is employed to model the fabrication sequences of the modulator [17], which contains waveguide etching, ion implantation, annealing and contact metallization. The program employs analytic implantation models and state-of-the-art point defect models that are calibrated to a large number of experiments to model ion implantation and diffusion. It generates files which contain exact impurity distribution after ion implantation and annealing. In our simulation, a dual primary distribution function which consists of two Pearson-IV distributions is selected to take the ion channeling effect into account [18]. The lateral straggling of implanted ions is specified by a Gaussian function with a depth-dependent standard deviation. A pair diffusion model is chosen to calculate the dopant diffusion in the rapid thermal annealing process, which is 1050 °C for 10s. After creating a virtual modulator by DIOS, we use a two-dimensional device simulation package DESSIS to calculate its electrical characteristic [17]. The program solves the coupled Poisson equation and the charge continuity equation for electrons and holes numerically. Its output results include carrier distributions at different biases, transient responses to given driven signals, and a small-signal equivalent circuit. Following semiconductor physics models are employed in our calculations:

- the Shockley-Read-Hall (SRH) recombination model with doping-dependent lifetime,
- the mobility model which includes the doping dependence effect and the high field saturation effect,
- the silicon band-gap narrowing model.

The carrier distribution is then converted to the distribution of refractive index through the free-carrier dispersion relationship at 1.55 μm [2]. Finally an optical model solver is used to calculate effective model index shifts and optical losses of the phase shifter at different bias voltages.

Table 1 presents an un-optimized implantation condition to
An implantation condition without any optimization

<table>
<thead>
<tr>
<th>Implantation window</th>
<th>Species</th>
<th>Dose (10^11/cm²)</th>
<th>Energy (keV)</th>
</tr>
</thead>
<tbody>
<tr>
<td>P++ (-1.8, -1.25)</td>
<td>B</td>
<td>1.5×10^11</td>
<td>20</td>
</tr>
<tr>
<td>P (-1.25, -0.1)</td>
<td>B</td>
<td>0.3×10^11</td>
<td>20</td>
</tr>
<tr>
<td>P+ (-0.1, 0.04)</td>
<td>B</td>
<td>2.2×10^11</td>
<td>30</td>
</tr>
<tr>
<td>N+ (0.04, 0.18)</td>
<td>P</td>
<td>2.2×10^11</td>
<td>100</td>
</tr>
<tr>
<td>N (0.18, 1.25)</td>
<td>P</td>
<td>0.3×10^11</td>
<td>70</td>
</tr>
<tr>
<td>N++ (1.25, 1.8)</td>
<td>P</td>
<td>1.5×10^10</td>
<td>70</td>
</tr>
</tbody>
</table>

Although the lateral straggling of implanted ions is inevitable, the detriment of it can be alleviated in some extent by proper positions of relevant implantation windows. To illustrate this point qualitatively, we make a linearly graded junction assumption here, under which the depletion width $w$ is reverse proportional to the cube root of the doping gradient: $w \propto d^{-1/3}$ [15]. Still neglecting the optical intensity variation inside the depletion region, we have $\Delta n_{eff} \propto \int_0^w axdx \propto d^{1/3}$, which means a steep junction is preferable. If we approximate the lateral straggling of implanted ions by Gaussian functions, the net dopant concentration in the transition region is the difference between two Gaussian distributions. Theoretically it can be deduced that if the distance between the two Gaussian peaks is equal to the sum of their standard deviation, the net dopant concentration has the largest gradient at its zero point. As a consequence the modulator would have the highest modulation efficiency. This can be confirmed by the simulation result in Fig. 3, which presents the performance of the phase shifter as a function of the implantation position for two doping doses. In this simulation, the P+ and N+ implantation regions are $(0.04 - x - w_1, 0.04 - x)$ and $(0.04 + x, 0.04 + x + w_1)$ respectively, where $w_1$ is the width of the implantation window. The two implantation windows with a gap of 2x are symmetrical about the plane $x=0.04 \mu$m in order to form a p-n junction here. Implantation energies here are the same as those listed in Table. 1, the resulting average lateral standard deviations of B and P are $\sigma_B=43$ nm and $\sigma_P=36$ nm respectively [18]. In Fig. 3 increasing the implantation dose does enhance the modulation efficiency as expected in section II. Fig. 3 also indicates that the optimal gap between the N+ and P+ windows is 40 nm for both doping doses, which is smaller than the value of $(\sigma_B + \sigma_P)$. The reason for this deviation is that the start point of a Gaussian-type lateral straggling always locates several tens of nanometers inside the corresponding implantation window. Since a proper gap between the N+ and P+ implantation windows benefits the modulation efficiency, we should take this point into account when designing the mask. Figure 3 also provides an insight into the impact of lithography alignment errors. Any misalignments between the P+ and N+ implantation windows can weaken the modulation efficiency according to the date in Fig. 3, so high resolution lithography is critical for such devices.
Fig. 3. Performance of the phase shifter versus the positions of the N+ and P+ implantation windows. (a) Dose=2.2×10^{13}/cm^2, w_1=140 nm, tilt angle=0°. (b) Dose =1.1×10^{13}/cm^2, w_2=250 nm, tilt angle=0°. The effect mode index change ∆n_{eff} is calculated at a reverse bias of 10 V.

Fig. 4. Influences of (a) the implantation energy and (b) the tilt angle on the performance of the phase shifter. (a) tilt angle=0°, energy=25 kev (B). Other parameters in simulation are: P+ (-0.12 nm, 0.02 nm), N+ (0.06 nm, 0.2 nm), Dose=2.2×10^{13}/cm^2.

Up to now we have optimized the implantation condition for the active region. For the ion implantations in the N, P, N++ and P++ regions, we directly adopt the conditions listed in Table. I. The characteristic of the final phase shifter is presented in Fig. 2(a): its effective mode index shift at a reverse bias of 10 V is ∆n_{eff}=0.0035, which leads to a figure of merit of 2.2 V·cm; the capacitance and the internal resistance per unit length at a reverse bias of 5 V are 0.205 fF/μm and 12 kΩ/μm respectively. The evolution of the extinction ratio with the phase shifter length is shown in Fig. 2(b), which indicates that the extinction ratio can be improved by 4.6 dB after the implantation condition optimization. Here we give an explanation about the implantation conditions of the N and P doped areas. The two areas are outside the active region, so most of their implantation parameters have little influence on the modulation except the dose. Enhancing the doping dose in the doped regions is 84.2 nm. However, the ultimate width is w_2=140 nm due to the limitation of the practical lithograph. In our simulation the implantation mask thickness is H=500 nm; thus the final irradiated width is w_{eff}=87.4 nm for a tilt angle of 6°. This value is quite close to the desired width of 84.2 nm, so a simple calculation about w_{eff} can well explain the curve in Fig. 4(b). According to the simulation result, we conclude that the optimal implantation energy and tilt angle for our structure are 25 kev (B) and 4° respectively.
the N and P regions can reduce the internal resistance and increase the optical loss, so a compromise should be made about this parameter. According to the simulation result listed above, the total internal resistance and capacitance of a 0.5 mm phase shifter are 24 Ω and 0.102 pF respectively. Since the output impedance of the RF power supply is 50 Ω, the 3-dB bandwidth given by the formula 1/(2πRC) is 21.1 GHz provided that a lumped electrode is applied. Compared with the reported modulators of the same phase shifter length [12], [19], the bandwidth of our structure is competitive. The internal resistance doesn’t limit the modulation bandwidth, so there is no need to reduce it with the penalty of increasing the optical loss.

IV. SIMPLIFIED DOPING PATTERN

The doping pattern in Fig. 1(a) consists of 6 different doped regions, so 6 ion implantation steps are required. In order to save the fabrication cost, we hope to reduce the implantation steps under the premise of not deteriorating the preformance of the device. This can be achieved by the structure presented in Fig. 5(a). The two areas between the active and the metal contacting regions are alternately doped along the longitudinal direction with a period of \( H \), so their equivalent implantation doses are the ratio of the practical doses to the filling factor of the doped channels. These doped channels are implanted simultaneously with the active region to save 2 implantation steps. We can tune the internal resistance by changing the filling factor. According to the simulation result in section III, a proper value of the filling factor can be 0.14. Another geometric parameter need to be defined is the period \( H \), which can affect the intrinsic response time of the modulator. The intrinsic response time is the time required for carriers to be swept out and to come back into the active region. It can be characterized by the current response of the device to a voltage pulse. Extending the period \( H \) leads to a longer route for the movement of free carriers, so it would increase the intrinsic response time. This point is evident in Fig. 5(b), which depicts transient current evolution with the time. The driven signal is a 10 V voltage pulse with a ramp time of 2 ps. Since a complete three-dimensional calculation requires too much simulation resources, the transient simulation here only calculates the two-dimensional planar structure in Fig. 5(a).

The transient current response in Fig. 5(b) is defined as the current density per unit length along the direction perpendicular to the substrate for a 2.2 mm phase shifter, thus the final driving current is the product of the current density and the height of the rib waveguide, which is 220 nm. Then the peak value of the driving current in Fig. 5(b) is several hundred milliamperes, which coincides with previous reports [8]. The current response in Fig. 5(b) is faster at the rising edge of the voltage pulse than that at the trailing edge, so the cutoff frequency limited by the intrinsic response time is \( f = 0.35 / T \) [8], where \( T \) is the response time of the current at the trailing edge of the voltage pulse. The response time is defined as the duration during which the current decreases from 90% to 10% of its peak value. In Fig. 5(b), \( T \) increases from 12.6 ps to 24.4 ps as the period \( H \) grows from 1 μm to 5 μm, thus the cutoff frequency falls from 27.8 GHz to 14.34 GHz. Since there are other limits for the modulation speed such as the RC constant, we suggest the period \( H \) should not exceed 5 μm for a high speed operation.

V. CONCLUSION

In this paper, we study the influence of the ion implantation condition on the performance of depletion-type silicon optical modulators. Through a comprehensive simulation about implantation parameters, such as the position, the energy and the tilt angle, the extinction ratio of final Mach-Zehnder modulator is enhanced by 4.6 dB, and its insertion loss is reduced from 3 dB to 2.47 dB. The improvement is achieved mainly by two approaches: at first, a gap between two specific implantation windows alleviates the detriment to the modulation efficiency caused by the lateral straggling of implanted ions. Secondly a tilt angle can relax the line width limit of the practical lithography on implantation windows, the final active region width can be slimmer than that defined in the implantation mask. Any extra optical loss at high doping concentrations can be minimized by using a tilt angle to equalize the active and the carrier depletion regions. Although the discussion in this paper is based on an ordinary lateral p-n junction based modulator, we believe that the technique in this paper can also be applied to other doping patterns as long as the optical modulation relies on carrier depletion. For example, if a
vertical junction is applied instead of the lateral junction [19], then due to the ion straggling in the direction perpendicular to the wafer surface, it is also necessary to separate the two distribution peaks of p and n type dopants by a gap in this direction. Of course, the gap now is determined by the implantation energy rather than the implantation position. A specific simulation analogical to that presented in this paper can give the optimal implantation energy. In addition the doping pattern in Fig. 5(a) can also be used to reduce the unnecessary optical loss for the vertical junction based modulator.

REFERENCES


[17] ISE Integrated Systems Engineering AG, Affolternstrasse 52, CH-8050 Zurich, Switzerland.
