ABSTRACT

While distributed video coding (DVC) has emerged as a new video coding paradigm, the compression performance of current systems is still low compared to conventional solutions such as H.264/AVC. While the latter uses many coding modes and an efficient mode decision strategy for choosing the best mode, in DVC, only a limited number of modes has been developed so far. Since encoder-side mode decision in DVC increases encoder’s complexity, in this paper, we introduce decoder-side mode decision choosing between bitplane WZ coding and bitplane intra coding. This strategy proves to be efficient, delivering rate gains up to 22% over DISCOVER, without increasing the complexity at the encoder.

Index Terms— Distributed video coding, mode decision, Wyner-Ziv coding

1. INTRODUCTION

Video compression is typically performed by exploiting redundancies in the frame sequence. The temporal redundancies are usually exploited through a process called motion estimation. While this highly-complex task is performed at the encoder in conventional solutions such as MPEG-2 and H.264/AVC, this task is assigned to the decoder in distributed video coding (DVC). As a result, the complexity of the encoder is low compared to the complexity of the decoder, which facilitates a range of applications including visual sensor networks and networked camcorders [1].

The most frequently adopted DVC architecture is the one proposed by Aaron et al. [2, 3]. In this system, the frame sequence is split into key frames and Wyner-Ziv (WZ) frames. Key frames are intra coded, i.e. without using other frames as references. For each WZ frame, a prediction is generated at the decoder using block-based motion estimation techniques. This decoder-side prediction is called the side information. Next, the encoder sends error correcting information calculated on the original frame, allowing the decoder to correct the

side information. This basic architecture has been extended by numerous researchers through the years. Some interesting side information generation techniques have been proposed in the context of DISCOVER [4], as well as by other researchers [5, 6, 7]. Information about the correlation between the side information at the decoder and the original at the encoder, is needed at the decoder when using high-performance channel coding techniques (e.g. LDPC or turbo coding) for correcting the side information. Online techniques for estimating this correlation have been proposed (e.g. [8, 9, 10]).

Despite these and many other contributions, the compression efficiency of DVC systems is still not comparable to state-of-the-art conventional video compression with encoder-side motion estimation, such as H.264/AVC. One of the possible reasons for this performance gap is the lack of an effective mode decision strategy. While a large number of possible predictors are generated in H.264/AVC, only a limited number of coding modes are currently used in DVC.

Most DVC techniques apply mode decision at the encoder. Typically, the encoder generates an estimation of the side information using low-complexity techniques, and mode decision is performed by comparing the original frame to this estimation. In PRISM, a block-based approach is followed, and blocks are classified as skip, intra or WZ, and coded accordingly. Mys et al. [11] use similar ideas to introduce a block-based skip mode in the popular architecture by Aaron et al. [3]. Ascenso et al. [12] use fast motion estimation techniques for encoder-side mode decision between intra mode and WZ mode. Intra blocks are coarsely quantized and used at the decoder to improve the quality of the side information.

One of the drawbacks of encoder mode decision in DVC is that there is a trade-off between encoder complexity and compression performance, since the best results will be achieved if the encoder has an accurate estimation of the side information. Therefore, in this paper, an intra mode is defined for which mode decision is performed at the decoder instead of at the encoder. As a result, the complexity of the encoder remains fairly the same.
After a description of the codec architecture in Sect. 2, the results of offline experiments are presented in Sect. 3. These experiments are then used to define a mode decision strategy (Sect. 4), which shows to be very effective (Sect. 5). Conclusions are provided in Sect. 6.

2. CODEC ARCHITECTURE

The codec architecture proposed in this paper is depicted in Fig. 1. This architecture is based on the DVC architecture developed at Stanford [3], with some important improvements adopted from more recent work (as will be discussed). The main novelty in this paper is the addition of a bitplane intra coding mode with decoder-side mode decision. The motivation for this extension is based on offline analysis, as described in Sect. 3. However, to understand the context of this analysis, we first discuss the operation of the codec in detail.

At the encoder, the frame sequence is split into key frames and WZ frames. Key frames are intra coded using H.264/AVC intra coding. WZ frames are partitioned into non-overlapping blocks of size 4-by-4, and each block is transformed using a discrete cosine transform (DCT). Coefficients at identical positions are grouped into coefficient bands, resulting in a total of 16 coefficient bands per color component. Next, each coefficient band is quantized by a quantizer having $2^{4k}$ levels. Bits at corresponding positions (e.g. all most significant bits) are grouped into so-called bitplanes, resulting into $M_k$ bitplanes per coefficient band. Each bitplane is then coded using one out of two options, for which information is received from the decoder. If the intra mode is selected by the decoder (1), a binary arithmetic coder is used to code the bitplane. This arithmetic coder applies an adaptive context model that is initialized to a uniform distribution before the bitplane is coded. The coded bitplane is then sent to the decoder. If the intra mode is selected by the decoder (1), parity bits are calculated through centroid reconstruction [14]. The decoded frame $W'$ is then obtained after applying the inverse DCT.

Sending modes from decoder to encoder introduces a small overhead. In our system, the 16 luma coefficient bands are each quantized to minimum three and maximum eight bits. This means that between 48 and 128 bitplane modes need to be communicated. Using one bit to signal WZ or intra, the overhead for a GOP of size 4 at 30 fps (as used in the experimental results) is between 1.08 and 2.88 kbps. This corresponds to a negligible bit rate overhead below 0.5%.

3. INTRODUCING A NON-WZ MODE

The motivation for using an additional bitplane mode is based on offline analysis. Note that the decoded bitplane in intra mode is always equal to the original bitplane, while only high reliability is obtained for the WZ mode. However, in practice the difference in distortion between both modes is negligible, so that only bit rate is considered in our analysis.

Offline tests have been performed. In a first pass, the arithmetic coding option is used exclusively. For each frame, and for each bitplane, the coding rate is stored in a file. In a second pass, the WZ coding option is used exclusively, and bit rates are stored in a similar fashion. To illustrate the evolution of these bit rates as a function of the frame index, as an example, results are generated for the least significant bitplane of the DC coefficient, for the first 129 frames of the Table Tennis sequence (CIF resolution, 30 fps). A GOP of size 4 is used, and a hierarchical coding structure is applied. This means that the sequence $I_1 - W_1 - W_2 - W_3 - I_2 - ...$ is decoded in the following order: $I_1 - I_2 - W_2 - W_3 - W_4 - ...$. Hence, two layers of WZ frames can be identified, depending on the distance between the reference frames used for generating the side information. The first layer contains frames $W_2, W_6$, etc., which use the decoded $I$ frames for generating the side information. For the second layer, containing frames $W_1, W_3, W_5, W_7$, etc., the reference frames are much closer (e.g. decoded frames $I'_1$ and $W'_2$ for $W_1$). The DC coefficient is quantized to 5 bits and mapped using a gray mapping. The intra quantization parameter is chosen so that intra frames and WZ frames have similar quality.

The results are depicted in Fig. 2, with separate graphs for each layer of WZ frames. An interesting observation is that the WZ coding mode is not always the best option. Instead, in some cases it appears to be more advantageous to use an intra coding mode, and code and decode without using the side information at all. This is due to inaccuracies in both the side information as well as the correlation noise estimation.

4. BITPLANE-LEVEL MODE DECISION

In this paper, mode decision is performed at the decoder instead of at the encoder. This is in favor of the DVC paradigm
Fig. 1. The proposed DVC architecture, featuring an additional bitplane intra coding mode with decoder-side mode decision.

![Diagram of the proposed DVC architecture](image)

![Number of bits vs Frame index for different coding methods](image)

Fig. 2. Bitplane intra coding versus bitplane WZ coding, for the least significant bitplane of the DC, for two different layers of WZ frames. The proposed online mode decision illustrates significant gains over using only the WZ mode.

<table>
<thead>
<tr>
<th>Frame index</th>
<th>Number of bits</th>
</tr>
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<tbody>
<tr>
<td>0</td>
<td>500</td>
</tr>
<tr>
<td>10</td>
<td>1000</td>
</tr>
<tr>
<td>20</td>
<td>1500</td>
</tr>
<tr>
<td>30</td>
<td>2000</td>
</tr>
<tr>
<td>40</td>
<td>2500</td>
</tr>
</tbody>
</table>

For the least significant bitplane of the DC, it will be best for bitplane $k$ in frame $F$, then chances are high that it will be best for bitplane $k$ in the next WZ frame (in the same layer of WZ frames). However, since only one mode is used for coding a particular bitplane in $F$, the bit rate for the other mode needs to be estimated. To this extent, the following procedure is used at the decoder to estimate the bit rate:

- if bitplane $k$ was WZ coded, the decoded bitplane is coded using the arithmetic coder,
- if bitplane $k$ was intra coded, a turbo coding/decoding strategy is adopted.

Hence, in both cases, an estimation of the bit rate is obtained through coding of the decoded bitplane, which is available at the decoder. This simple strategy is effective, as illustrated by the actual rate spent in the proposed system, switching between bitplane intra coding and bitplane WZ coding (Fig. 2).

5. RESULTS

Tests have been conducted on several sequences in CIF resolution at 30 fps. A GOP of size 4 is used and only the luma component is coded, to allow comparing with the DISCOVER codec. As a reference, results have been generated using the system described in this paper, but excluding the intra mode. This configuration is labeled “previous work”.

The results in Fig. 3 indicate that adding a bitplane intra mode increases performance significantly. Average Bjøntegaard [15] rate gains over DISCOVER can be observed of 22% for Foreman, and 14% for Coastguard. The gain is the largest at high rates, where the correlation between the side information bitplanes and the original bitplanes is poor.

Good results are obtained for other sequences as well (not depicted due to space constraints), leading to average rate gains over DISCOVER of 14% for Mother and Daughter, and 8% for Table Tennis.

6. CONCLUSIONS

A technique for decoder-side mode decision has been introduced, choosing between coding a bitplane using a WZ approach or an intra approach. The results indicate that DVC
system performance can be improved by using different coding modes, without the need for side information estimation at the encoder.

7. REFERENCES


